

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 13692	Application Number 09/870,841			
				Applicant(s) Richard S. NORMAN et al.				
				Filing Date June 1, 2001	Group Art Unit 2661			
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
								
RECEIVED								
AUG 15 2003								
Technology Center 2600								
<b>FOREIGN PATENT DOCUMENTS</b>								
EXAMINER	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
<i>Alee</i>	1.	WO 98/26539	06-18-1998	International				
<i>Alee</i>	2.	EP 1 051 001 A2	11-08-2000	Europe				
<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
<i>Alee</i>	1.	Partial International Search; PCT/CA02/00810						
EXAMINER: <i>Christie</i>				DATE CONSIDERED <i>08 NOV, 2004</i>				

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EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Alee	1.	U.S. 5,790,539	08-04-1998	Chao et al.			
Alee	2.	U.S. 5,831,980	11-03-1998	Varma et al.			
Alee	3.	U.S. 6,069,895	05-30-2000	Ayandeh			
<b>RECEIVED</b>							
OCT 27 2003							
Technology Center 2600							
<b>FOREIGN PATENT DOCUMENTS</b>							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translates
Alee	4.	EP 0 241 152 A2	10-14-1987	EUROPE			YES NO
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Pages, Etc.)							
Alee	5.	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987					
Alee		International Search Report PCT/CA02/00810, August 1, 2003					
EXAMINER <i>Alee</i>				DATE CONSIDERED 08 NOV 2004			

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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**INFORMATION DISCLOSURE CITATION**  
*(Use several sheets if necessary)*

Docket Number (Optional)	13602	Application Number	1017 U 870341
Applicant(s)	Richard S. NORMAN et al.		
Filing Date	01/01/91		
		Group Art Unit	35

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EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE - IF APPROPRIATE
Lee	1.	U.S. 5,008,878	16/04/1991	Ahmadi et al.			
Lee	2.	U.S. Re. 34,755	11/10/1994	Eng et al.			
Lee	3.	U.S. Re. 34,811	27/12/1994	Eng et al.			
Lee	4.	U.S. 5,361,257	01/11/1994	Petersen			
Lee	5.	U.S. 5,467,347	14/11/1995	Petersen			
Lee	6.	U.S. 5,999,527	07/12/1999	Petersén			
Lee	7.	U.S. 5,787,084	28/07/1998	Hoang et al.			
Lee	8.	U.S. 5,043,980	27/08/1991	Day, Jr. et al.			
Lee	9.	U.S. 6,144,662	07/11/2000	Colmant et al.			
Lee	10.	U.S. 5,938,736	17/08/1999	Muller et al.			
Lee	11.	U.S. 5,247,513	21/09/1993	Henrion et al.			

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						YES	NO
Lee	12. EP 0 680 173 A1	02/11/1995	EUROPE				

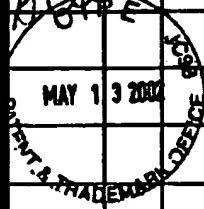
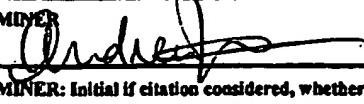
**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

Lee	13.	Nick McKeown et al. "The Tiny Tera: a Packet Switch Core"; Departments of Electrical Engineering and Computer Science, Stanford University; August 1996; <a href="http://tiny-tera.stanford.edu/tiny-tera/index.html">http://tiny-tera.stanford.edu/tiny-tera/index.html</a>

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				Applicant(s) Richard NORMAN et al.			
				Filing Date June 1, 2001		Group Art Unit 2661 2664	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Hlee	1.	U.S. 4,849,751	18/07/1989	Barber et al.			RECEIVED
Hlee	2.	U.S. 5,072,366	10/12/1991	Simcoe			AUG 31 2001
Hlee	3.	U.S. 4,955,020	04/09/1990	Stone et al.			Technology Center 2600
<b>FOREIGN PATENT DOCUMENTS</b>							
REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
						YES	NO
						YES	NO
						YES	NO
<b>OTHER DOCUMENTS</b> (Including Author, Title, Date, Pertinent Pages, Etc.)							
Hlee	4.	French, R., Architectural Consideration for Internet Routers; retrieved from the internet guideline to file; Internet URL www.cise.usf.edu/rfrench, accessed July 23, 2001;					
Hlee	5.	Joseph Desposito; Router-On-A-Chip Manages Network Traffic with Wire-Speed QoS; Electronic Design; May 1, 2000; pp 64-65-66;					
EXAMINER: <i>Anderson</i>				DATE CONSIDERED <i>08 Nov. 2004</i>			
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				Applicant(s) Richard S. Norman et al.		Filing Date 06/01/2001	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>Ree</i>	L	U.S. 5,377,182	12/27/1994	Monacos			
<i>Ree</i>	L	U.S. 5,278,548	01/11/1994	Haber			
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						YES	NO
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<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination NORMAN ET AL.	
		Examiner Andrew C Lee	Art Unit 2664	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,731,631 B1	05-2004	Chang et al.	370/388
	B	US-6,804,731 B1	10-2004	Chang et al.	710/53
	C	US-6,754,216 B1	06-2004	Wong et al.	370/395.42
	D	US-6,424,659 B2	07-2002	Viswanadham et al.	370/469
	E	US-6,735,173 B1	05-2004	Lenoski et al.	370/235
	F	US-6,134,217	10-2000	Stiliadis et al.	370/232
	G	US-6,778,529 B1	08-2004	Field et al.	370/353
	H	US-6,751,562 B1	06-2004	Blackett et al.	702/61
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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<i>Ande</i>		U.S. 5,790,539	08-04-1998	Chao et al.			
<i>Ande</i>	2	U.S. 5,831,980	11-03-1998	Varma et al.			
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<i>Ande</i>	4	EP 0 241 152 A2	10-14-1987	EUROPE			
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<i>Ande</i>	5	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS, SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987					
<i>Ande</i>	6	International Search Report PCT/CA02/00810, August 1, 2003					
EXAMINER <i>Ande</i>				DATE CONSIDERED <i>Sep 03, 2005</i>			
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Alee	5.	Joseph Desposito; Router-On-A-Chip Manages Network Traffic with Wire-Speed QoS; Electronic Design; May 1, 2000; pp 64-65-66;					
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Applicant(s) <b>Richard S. Norman et al.</b>	
Filing Date <b>06/01/2001</b>	Group Art Unit

## **U.S. PATENT DOCUMENTS**

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**OTHER DOCUMENTS** (*Including Author, Title, Date, Pertinent Pages, Etc.*)

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**Form PTO-A820**  
**(also form PTO-1449)**

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**INFORMATION DISCLOSURE CITATION**  
*(Use several sheets if necessary)*

Docket Number (Optional) <b>13692</b>	Applicant's Number <b>09/870,841</b>
Apprise(s) <b>Richard S. NORMAN et al.</b>	
Filing Date <b>June 1, 2001</b>	Group Art Unit <b>2661</b>

**U.S. PATENT DOCUMENTS**

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<b>RECEIVED</b>							
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AUG 15 2003

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						YES	NO
1.	WO 98/26539	06-18-1998	International				
2.	EP 1 051 001 A2	11-08-2000	Europe				

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*[Signature]* 1 Partial International Search; PCT/CA02/00810

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